

# Pointer manipulation

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Pseudo assembly code

```
D = *p
```

RAM		
0	257	p
1	1024	q
2	1765	
...	...	
256	19	
257	23	
258	903	
...	...	
1024	5	
1025	12	
1026	-3	
...	...	

Notation:

\*p // the memory location that p points at

# Pointer manipulation

Pseudo assembly code

```
D = *p           // D becomes 23
```

In Hack:

@p

A=M

D=M

RAM		
0	257	p
1	1024	q
2	1765	
...	...	
256	19	
257	23	
258	903	
...	...	
1024	5	
1025	12	
1026	-3	
...	...	

Notation:

\*p // the memory location that p points at

# Pointer manipulation

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Pseudo assembly code

```
D = *p           // D becomes 23  
  
p--  
D = *p
```

RAM		
0	257	p
1	1024	q
2	1765	
...	...	
256	19	
257	23	
258	903	
...	...	
1024	5	
1025	12	
1026	-3	
...	...	

## Notation:

\*p      // the memory location that p points at

x++     // increment:  $x = x + 1$

x--     // decrement:  $x = x - 1$

# Pointer manipulation

---

Pseudo assembly code

```
D = *p           // D becomes 23
p--              // RAM[0] becomes 256
D = *p           // D becomes 19
```

RAM		
0	257	p
1	1024	q
2	1765	
...	...	
256	19	
257	23	
258	903	
...	...	
1024	5	
1025	12	
1026	-3	
...	...	

## Notation:

\*p // the memory location that p points at

x++ // increment:  $x = x + 1$

x-- // decrement:  $x = x - 1$

# Pointer manipulation

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Pseudo assembly code

```
D = *p           // D becomes 23
p--              // RAM[0] becomes 256
D = *p           // D becomes 19
*q = 9
q++
```

RAM		
0	257	p
1	1024	q
2	1765	
...	...	
256	19	
257	23	
258	903	
...	...	
1024	5	
1025	12	
1026	-3	
...	...	

## Notation:

\*p // the memory location that p points at

x++ // increment:  $x = x + 1$

x-- // decrement:  $x = x - 1$

# Pointer manipulation

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Pseudo assembly code

```
D = *p           // D becomes 23
p--              // RAM[0] becomes 256
D = *p           // D becomes 19
*q = 9           // RAM[1024] becomes 9
q++              // RAM[1] becomes 1025
```

RAM		
0	257	p
1	1024	q
2	1765	
...	...	
256	19	
257	23	
258	903	
...	...	
1024	5	
1025	12	
1026	-3	
...	...	

## Notation:

\*p // the memory location that p points at

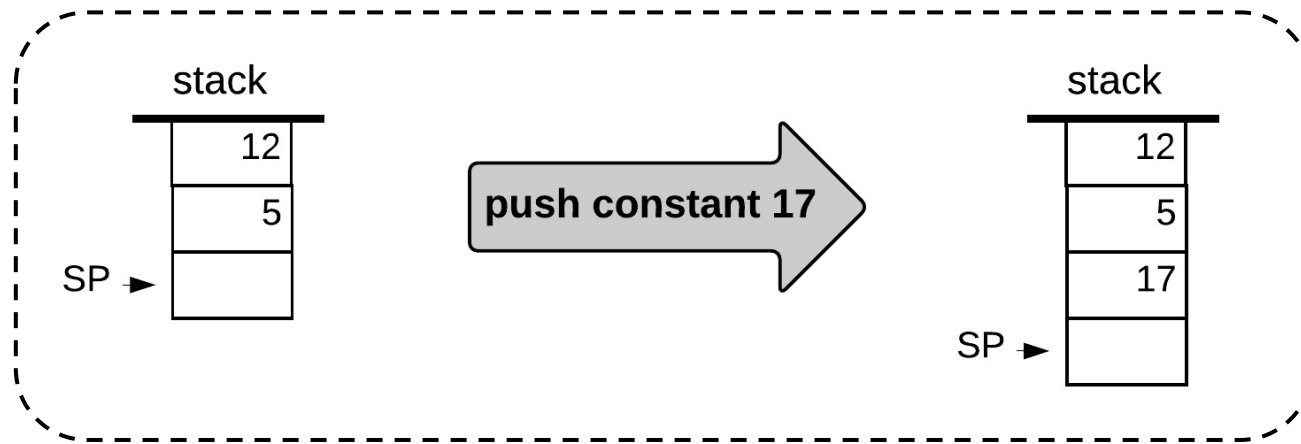
x++ // increment:  $x = x + 1$

x-- // decrement:  $x = x - 1$

# Stack implementation

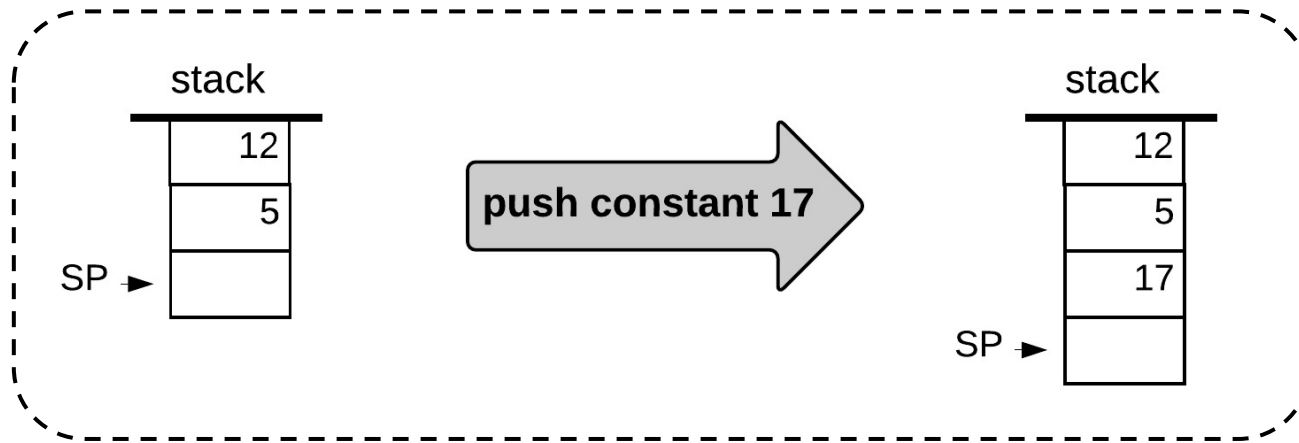
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Abstraction:



# Stack implementation

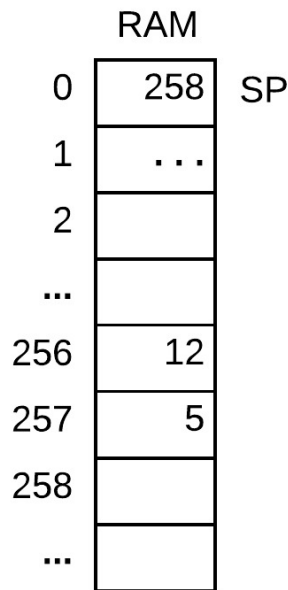
## Abstraction:



## Implementation:

Assumptions:

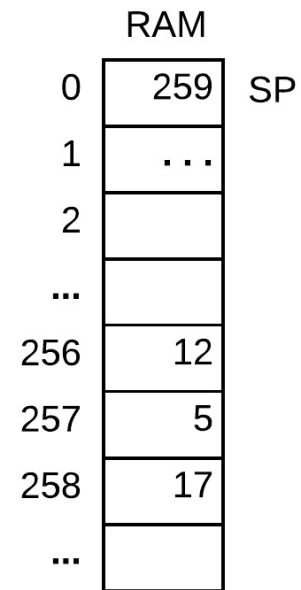
- SP stored in RAM[0]
- Stack base addr = 256



Logic:

```
*SP = 17  
SP++
```

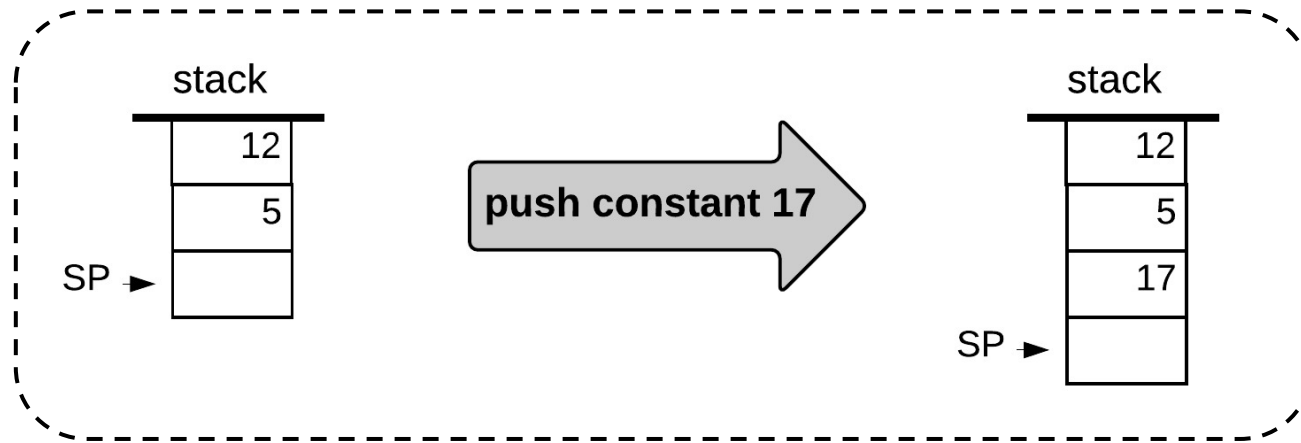
Hack assembly:





# Stack implementation

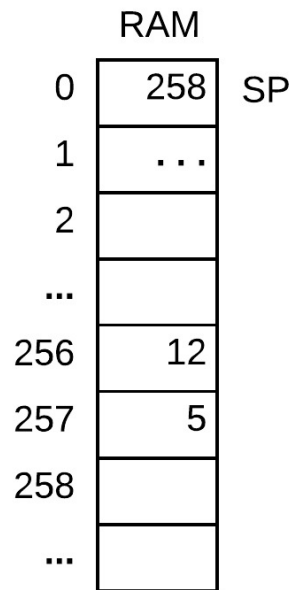
## Abstraction:



## Implementation:

Assumptions:

- SP stored in RAM[0]
- Stack base addr = 256

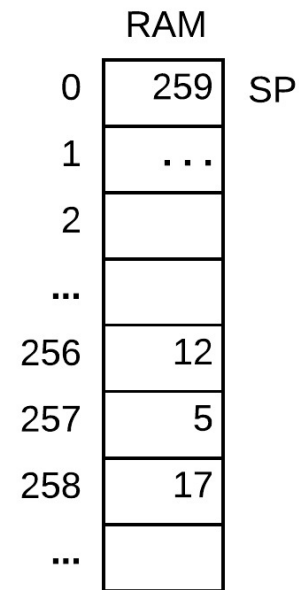


Logic:

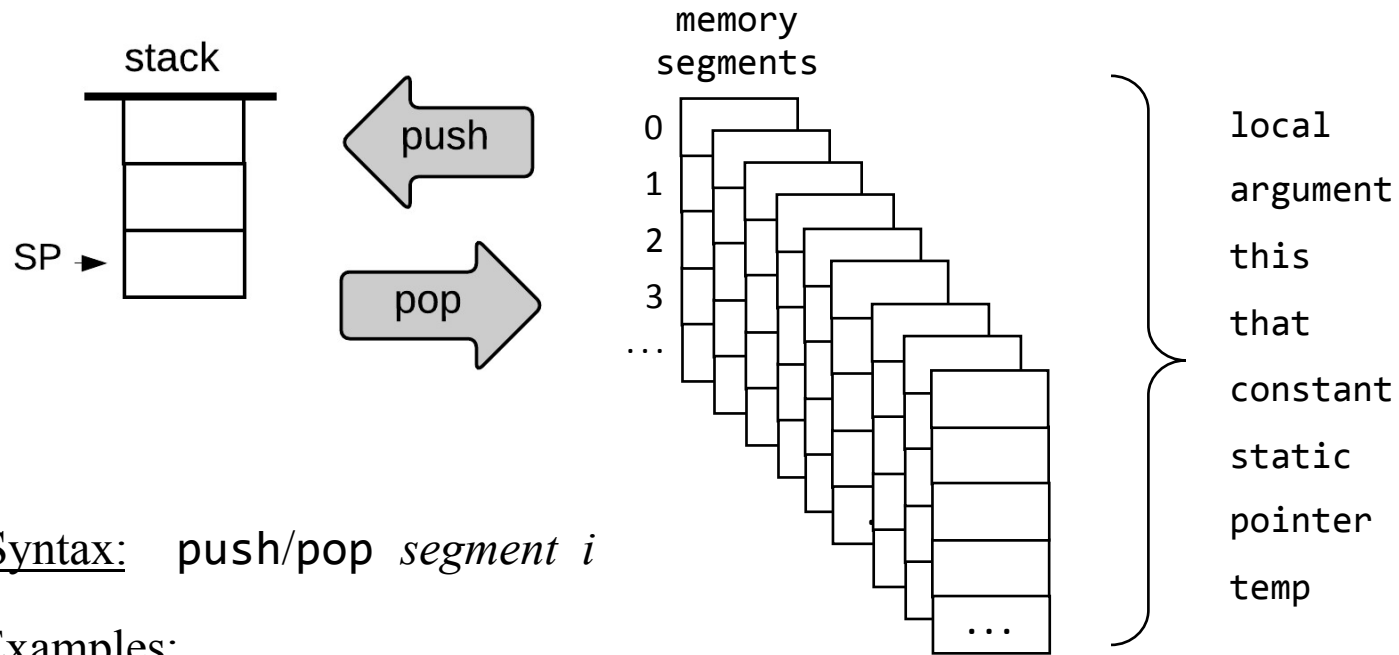
```
*SP = 17  
SP++
```

Hack assembly:

```
@17 // D=17  
D=A  
@SP // *SP=D  
A=M  
M=D  
@SP // SP++  
M=M+1
```



# Memory segments (abstraction)

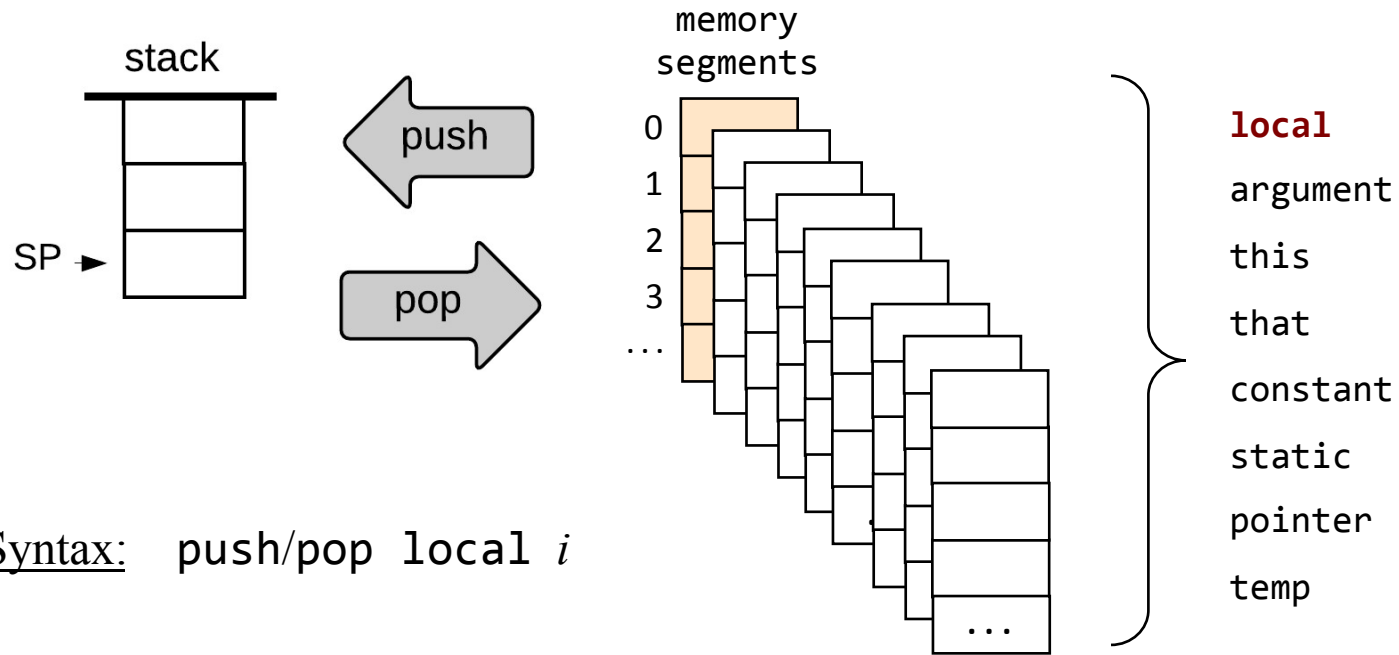


Syntax: push/pop *segment i*

Examples:

- push constant 17
- pop local 2
- pop static 5
- push argument 3
- pop this 2
- ...

# Implementing `push/pop local i`



Syntax: `push/pop local i`

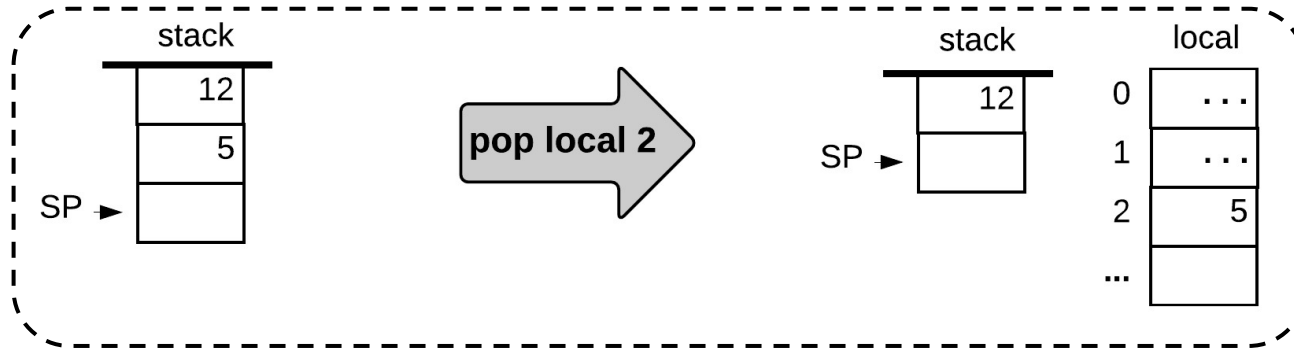
## Why do we need a `local` segment?

When the compiler translates high-level code into VM code...

- high-level operations on *local variables* are translated into VM operations on the entries of the segment `local`
- We now turn to describe how the `local` segment can be realized on the host platform.

# Implementing `pop local i` (example)

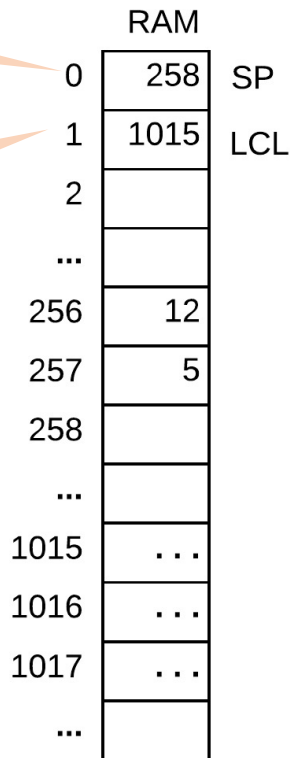
Abstraction:



stack pointer

base address of the local segment

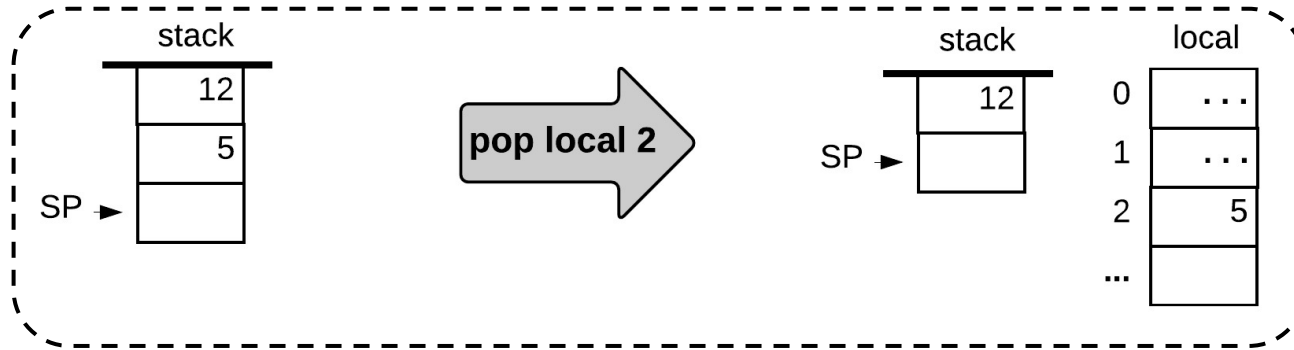
Implementation:



the local segment is stored somewhere in the RAM

# Implementing `pop local i` (example)

Abstraction:



stack pointer

base address of the local segment

Implementation:

the local segment is stored somewhere in the RAM

RAM	
0	258
1	1015
2	
...	
256	12
257	5
258	
...	
1015	...
1016	...
1017	...
...	

SP points to address 0, LCL points to address 1.

Implementation:

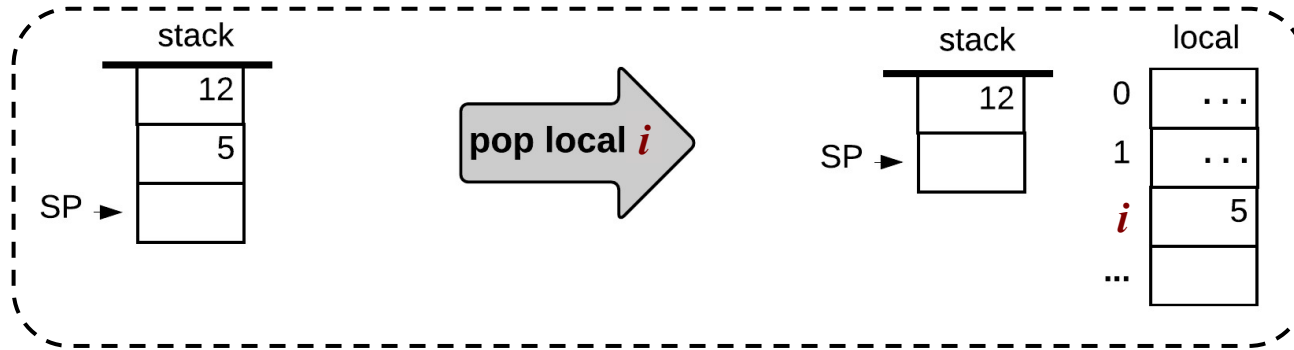
```
addr=LCL+2, SP--, *addr=*SP
```

RAM	
0	257
1	1015
2	
...	
256	12
257	5
258	
...	
1015	...
1016	...
1017	5
...	

SP points to address 0, LCL points to address 1.

# Implementing `pop local i`

Abstraction:



stack pointer

base address of the local segment

Implementation:

the local segment is stored somewhere in the RAM

RAM	
0	258
1	1015
2	
...	
256	12
257	5
258	
...	
1015	...
1016	...
1017	...
...	

SP points to address 0. LCL points to address 1.

Implementation:

```
addr=LCL+i, SP--, *addr=*SP
```

Hack assembly:

You write it!

RAM	
0	257
1	1015
2	
...	
256	12
257	5
258	
...	
1015	...
1016	...
1017	5
...	

SP points to address 0. LCL points to address 1.

# Implementing `push/pop local i`

VM code:

```
pop local i
```

```
push local i
```

VM Translator

Assembly pseudo code:

```
addr = LCL + i, SP--, *addr = *SP
```

```
addr = LCL + i, *SP = *addr, SP++
```

stack pointer

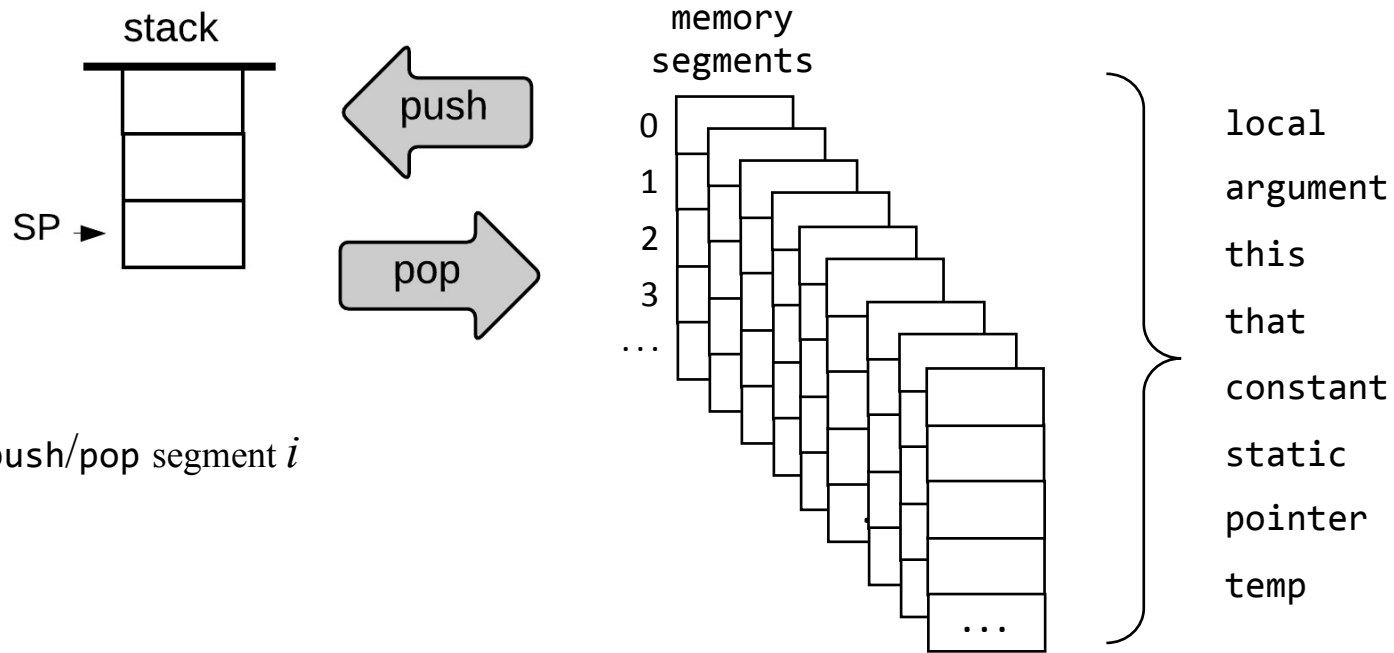
base address of  
the local segment

Implementation:

the local segment  
is stored some-  
where in the RAM

	RAM	
0	258	SP
1	1015	LCL
2		
...		
256	12	
257	5	
258		
...		
1015	...	
1016	...	
1017	...	
...		

# Memory segments

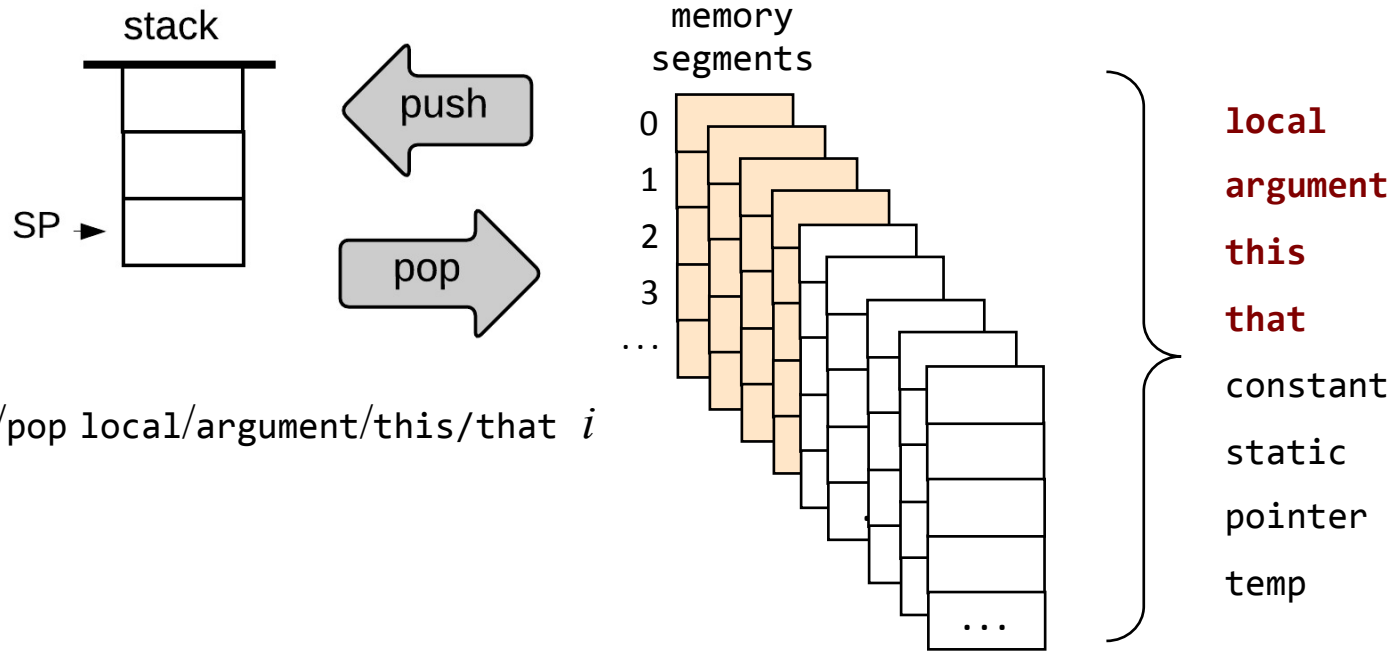


Syntax: push/pop segment  $i$

- We know how to implement push/pop local  $i$
- We now turn to implementing push/pop operations on the segments argument, this, and that.



# Implementing `push/pop local/argument/this/that i`



Syntax: `push/pop local/argument/this/that i`

# Implementing `push/pop local/argument/this/that i`

VM code:

```
push segment i
```

```
pop segment i
```

VM translator

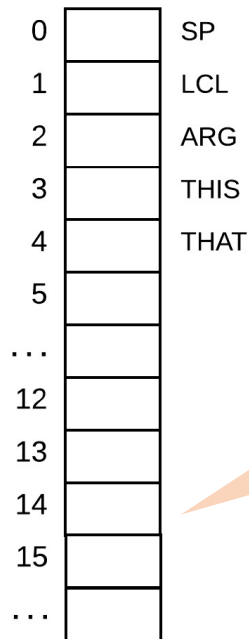
Assembly pseudo code:

```
addr = segmentPointer + i, *SP = *addr, SP++
```

```
addr = segmentPointer + i, SP--, *addr = *SP
```

`segment = {local, argument, this, that}`

Host RAM

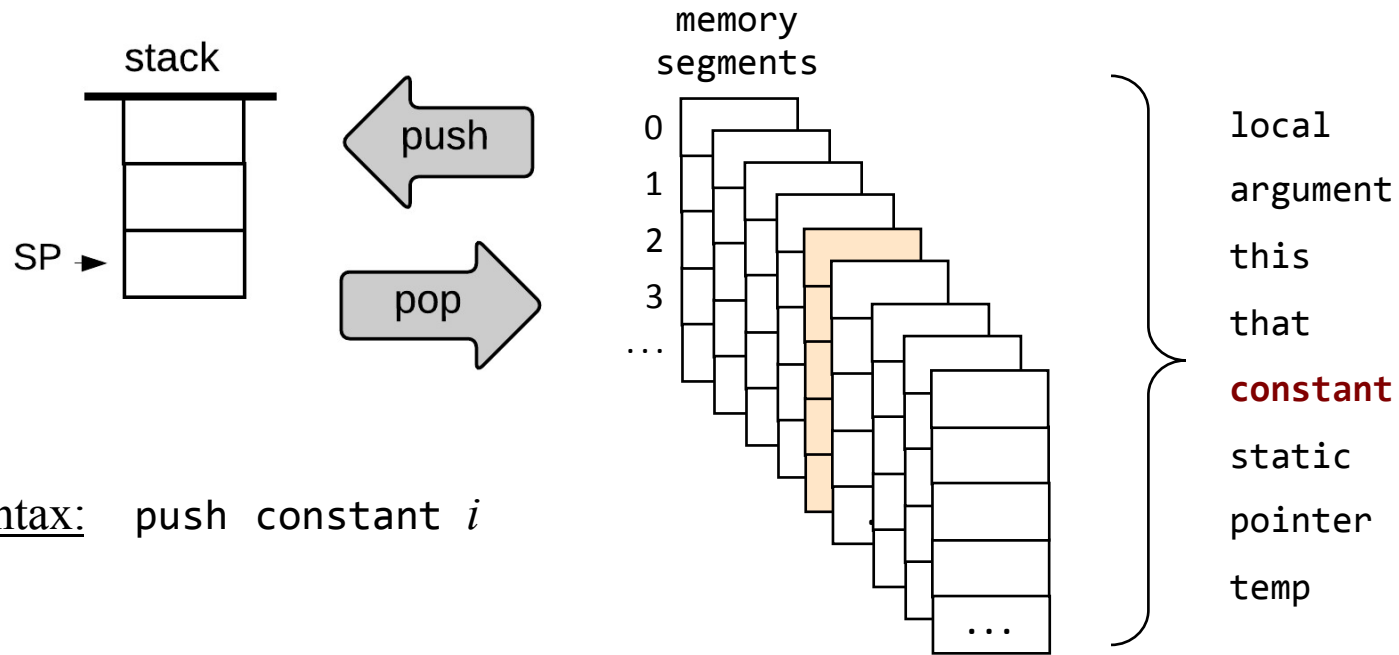


base addresses of the four segments are stored in these pointers

the four segments are stored somewhere in the RAM

- `push/pop local i`
  - `push/pop argument i`
  - `push/pop this i`
  - `push/pop that i`
- are implemented precisely the same way

# Implementing push constant $i$



Syntax: push constant  $i$

Why do we need a constant segment?

Because we need to represent constants somehow in the VM level.

When the compiler translates high-level code into VM code...

- high-level operations on the *constant  $i$*  are translated into VM operations on the segment entry *constant  $i$*
- This syntactical convention will make sense when we write the compiler.

# Implementing push constant $i$

---

VM code:

```
push constant  $i$ 
```

VM Translator



Assembly psuedo code:

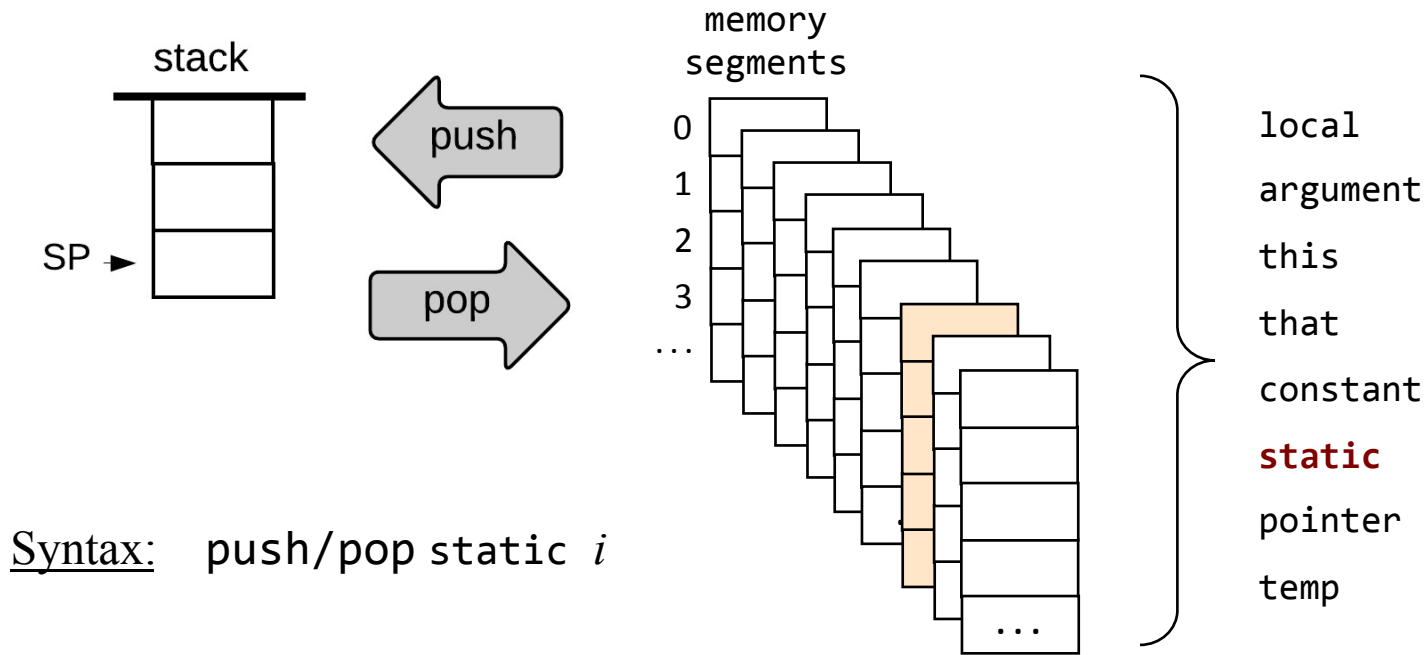
```
*SP =  $i$ , SP++
```

(no pop constant operation)

Implementation:

Supplies the specified constant

# Implementing `push/pop static i`



Syntax: `push/pop static i`

## Why do we need a static segment?

When translating high-level code into VM code, the compiler...

- high-level operations on *static* variables are translated into VM operations on entries of the segment *static*
- We now turn to discuss how the *static* segment is realized on the host platform.

# Implementing `push/pop static i`

VM code:

```
// File Foo.vm
...
pop static 5
...
pop static 2
...
```

VM translator

Generated assembly code:

```
...
// D = stack.pop (code omitted)
@Foo.5
M=D
...
// D = stack.pop (code omitted)
@Foo.2
M=D
...
```

The challenge:

static variables should be seen by all the methods in a program

Solution:

Store them in some “global space”:

- Have the VM translator translate each VM reference `static i` (in file `Foo.vm`) into an assembly reference `Foo.i`

# Implementing `push/pop static i`

VM code:

```
// File Foo.vm
...
pop static 5
...
pop static 2
...
```

VM translator

Generated assembly code:

```
...
// D = stack.pop (code omitted)
@Foo.5
M=D
...
// D = stack.pop (code omitted)
@Foo.2
M=D
...
```

The challenge:

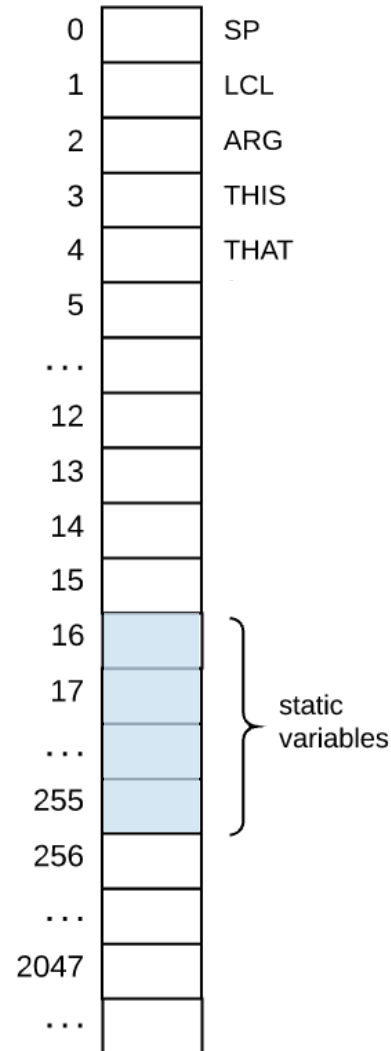
static variables should be seen by all the methods in a program

Solution:

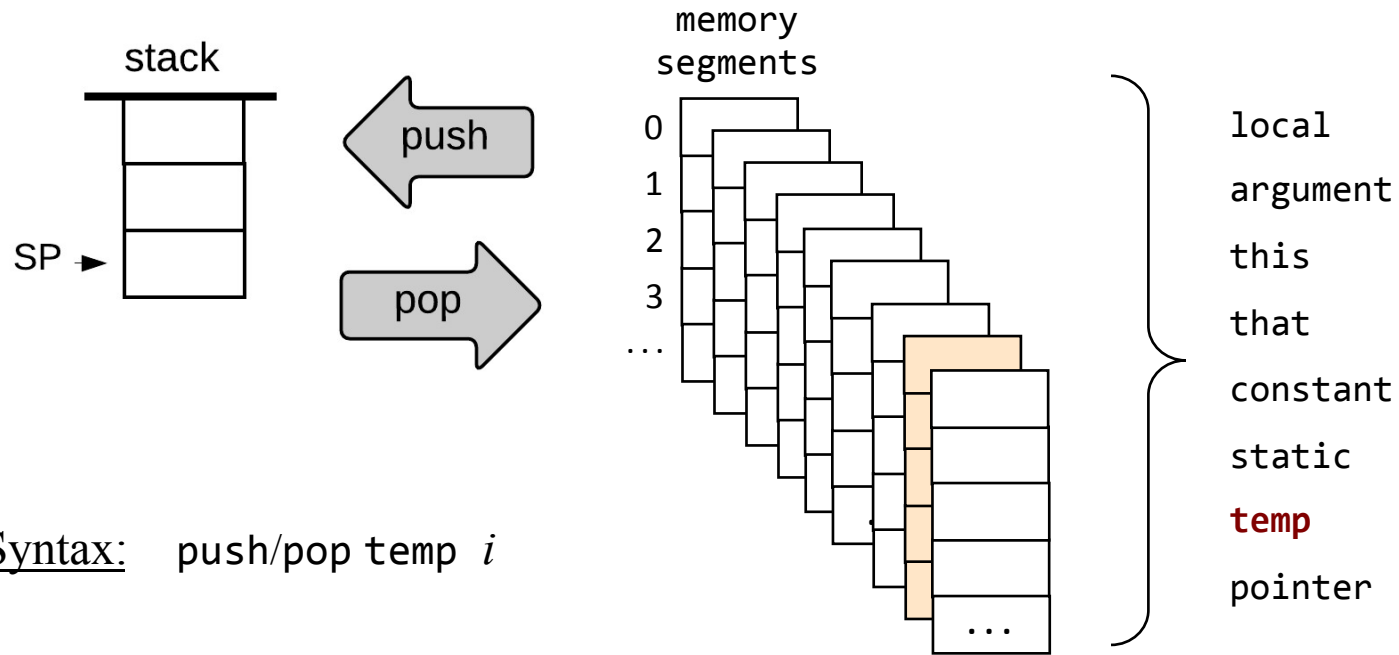
Store them in some “global space”:

- Have the VM translator translate each VM reference `static i` (in file `Foo.vm`) into an assembly reference `Foo.i`
- Following assembly, the Hack assembler will map these references onto `RAM[16]`, `RAM[17]`, ..., `RAM[255]`
- Therefore, the entries of the `static` segment will end up being mapped onto `RAM[16]`, `RAM[17]`, ..., `RAM[255]`, in the order in which they appear in the program.

Hack RAM



# Implementing `push/pop temp i`



Syntax: `push/pop temp i`

## Why do we need the temp segment?

- So far, all the variable kinds that we discussed came from the source code
- Sometimes, the compiler needs to use some working variables of its own
- Our VM provides 8 such variables, stored in a segment named `temp`.



# Implementing `push/pop temp i`

---

VM code:

`push temp i`

`pop temp i`

Hack RAM

0		SP
1		LCL
2		ARG
3		THIS
4		THAT
5		
...		
12		
13		
14		
15		
16		
...		
255		

# Implementing `push/pop temp i`

VM code:

`push temp i`

`pop temp i`

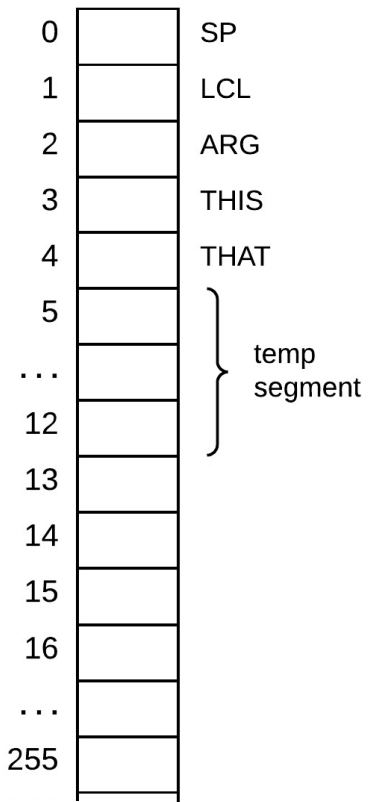
VM Translator

Assembly psuedo code:

`addr = 5 + i, *SP = *addr, SP++`

`addr = 5 + i, SP--, *addr = *SP`

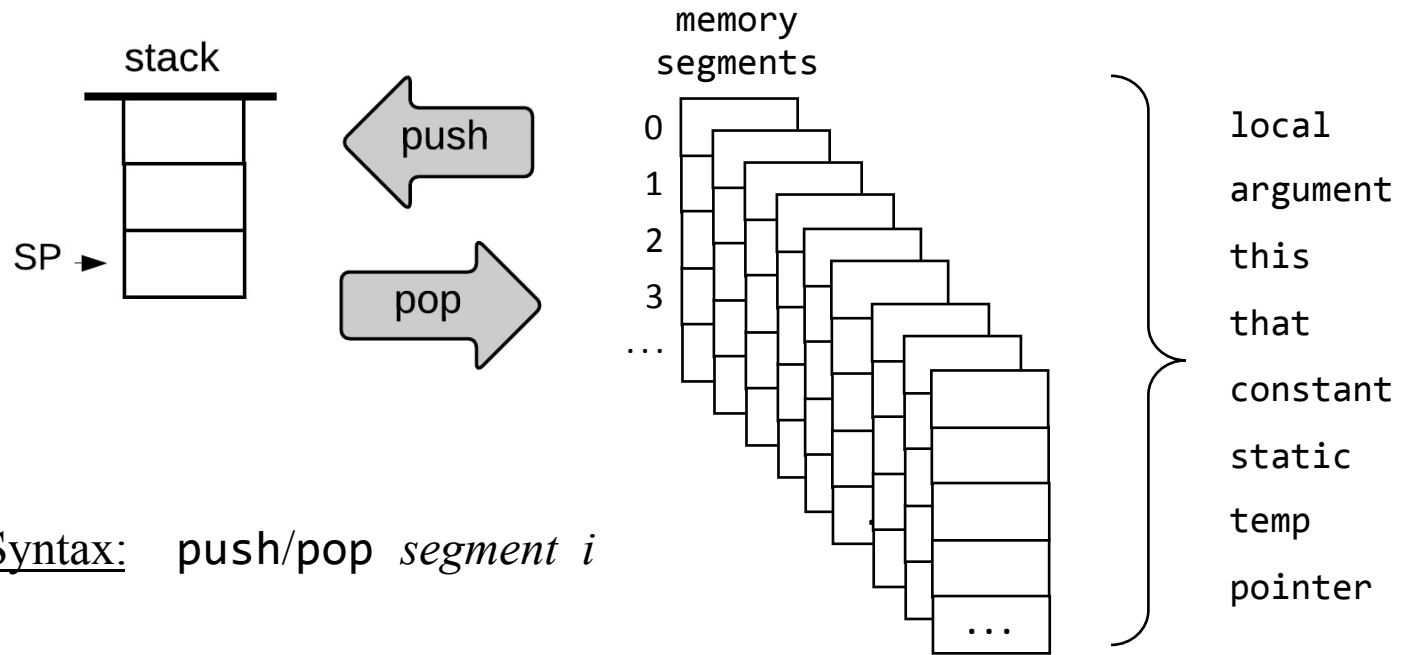
Hack RAM



A fixed, 8-place memory segment,  
stored in RAM locations 5 to 12

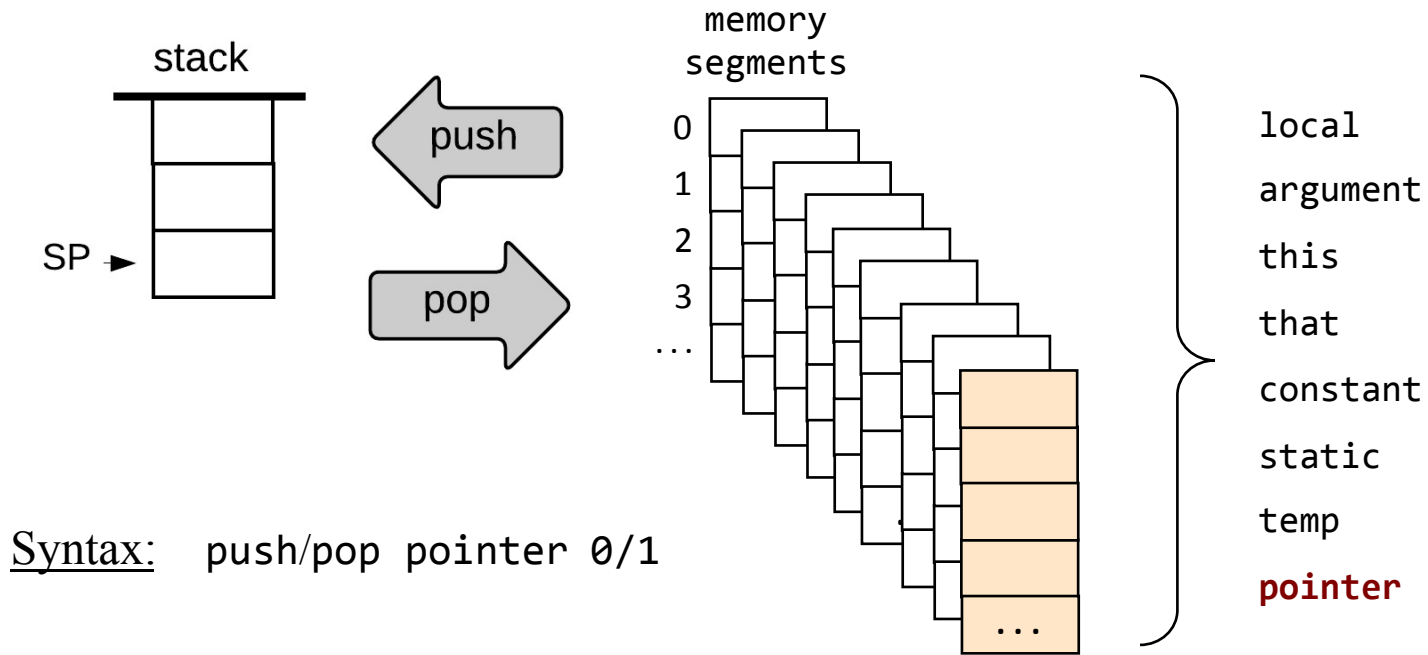
# Memory segments

---



Syntax: push/pop *segment i*

# Implementing push/pop pointer 0/1



## Why do we need the pointer segment?

- We use it for storing the base addresses of the segments `this` and `that`
- The need for this will become clear when we'll write the compiler.

# Implementing push/pop pointer 0/1

---

VM code:

```
push pointer 0/1
```

```
pop pointer 0/1
```

VM Translator

Assembly psuedo code:

```
*SP = THIS/THAT, SP++
```

```
SP--, THIS/THAT = *SP
```

A fixed, 2-place segment:

- accessing pointer 0 should result in accessing THIS
- accessing pointer 1 should result in accessing THAT

Implementation:

Supplies THIS or THAT // (the base addresses of this and that).